ABSTRACT OF THE DISCLOSURE

A current switch. The novel current switch includes a differential pair of transistors Q1 and Q2, a pair of cascode transistors QA and QB coupled to Q1 and Q2, respectively, and a circuit for maintaining QA and QB in an 'on' state regardless of the states of Q1 and Q2. The circuit for keeping QA and QB on includes first and second current sources adapted to supply first and second trickle currents to the emitters of QA and QB, respectively. The bases of QA and QB are connected in common to a voltage source VREF4, which, in an illustrative embodiment, is implemented using a Schottky diode for lower impedance. The circuit for driving Q1 and Q2 may also be implemented using a current switch with trickle current, cascode transistors Q14 and Q15 to further improve settling times.

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